

FIG. 1

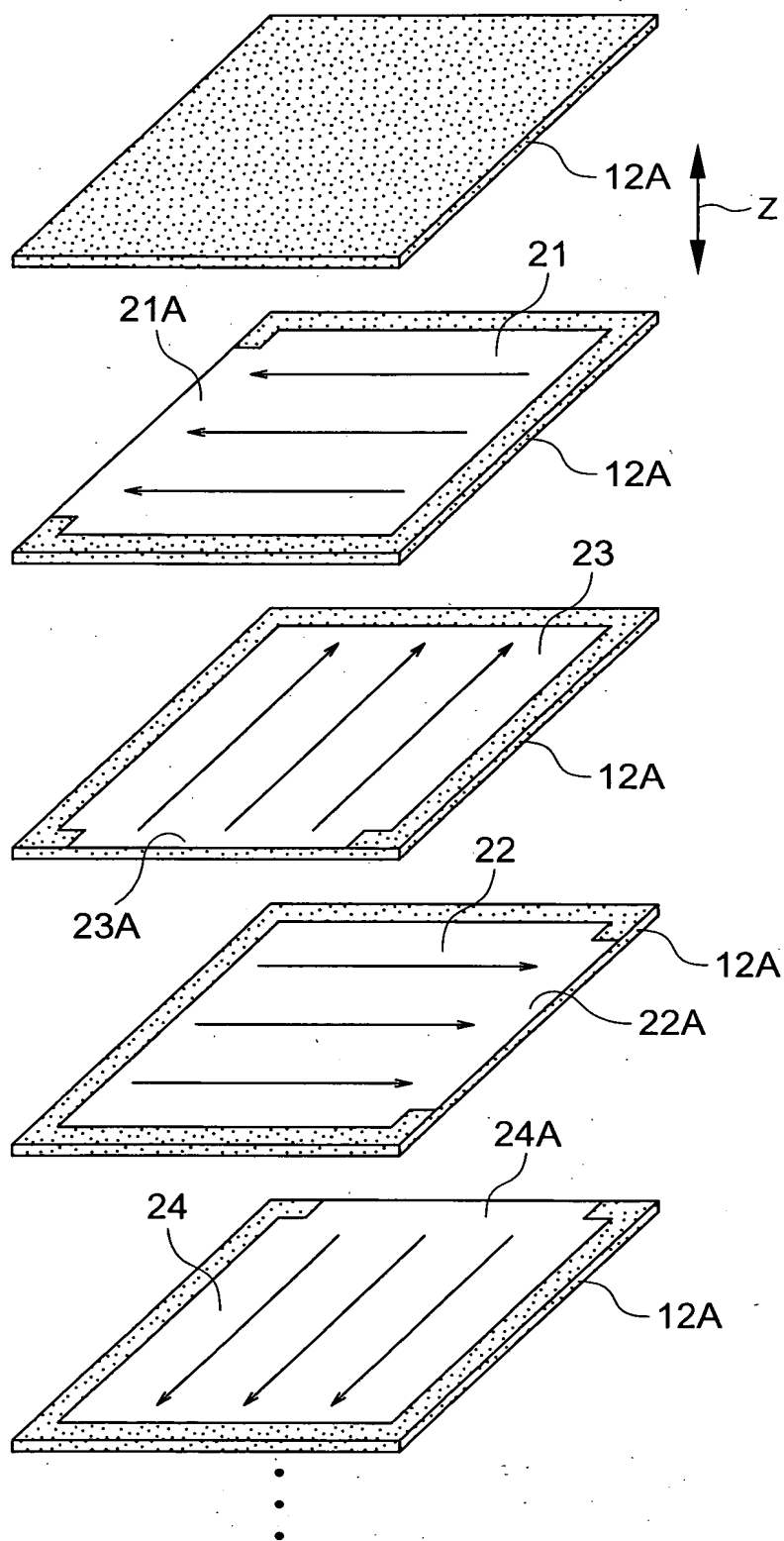


FIG. 2

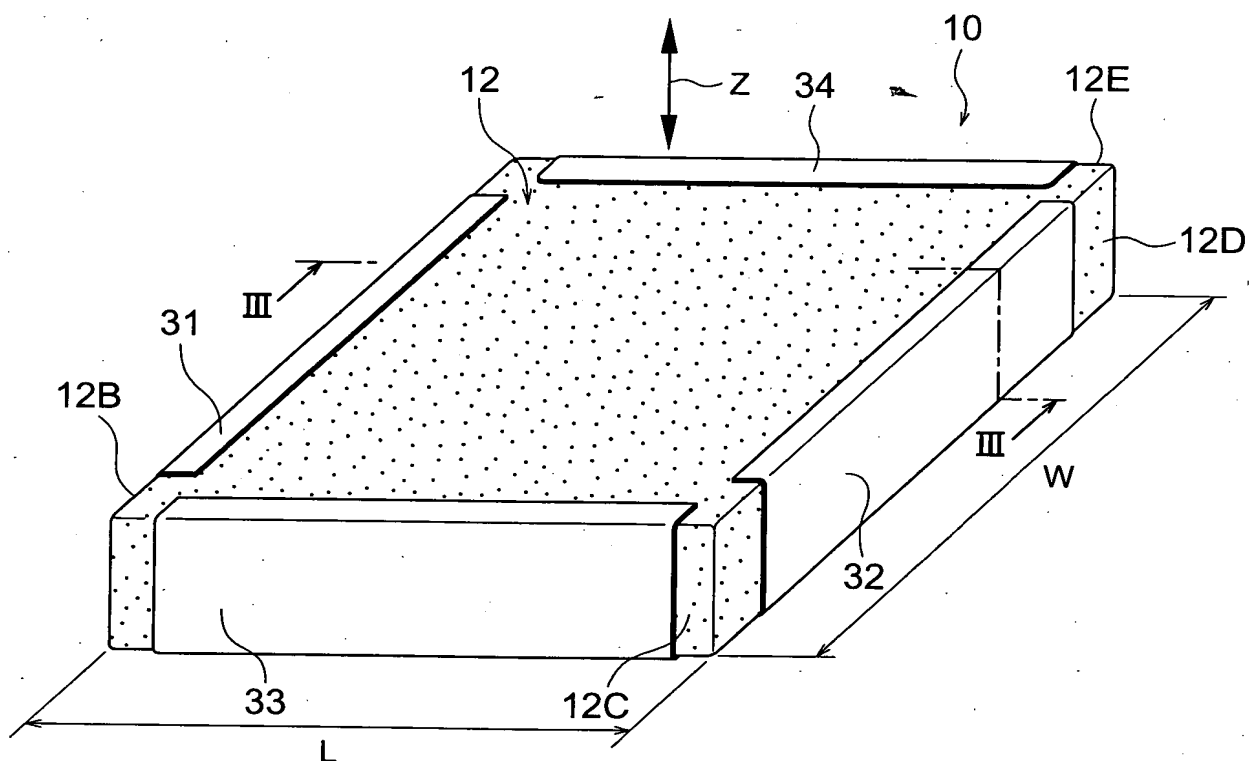


FIG. 3

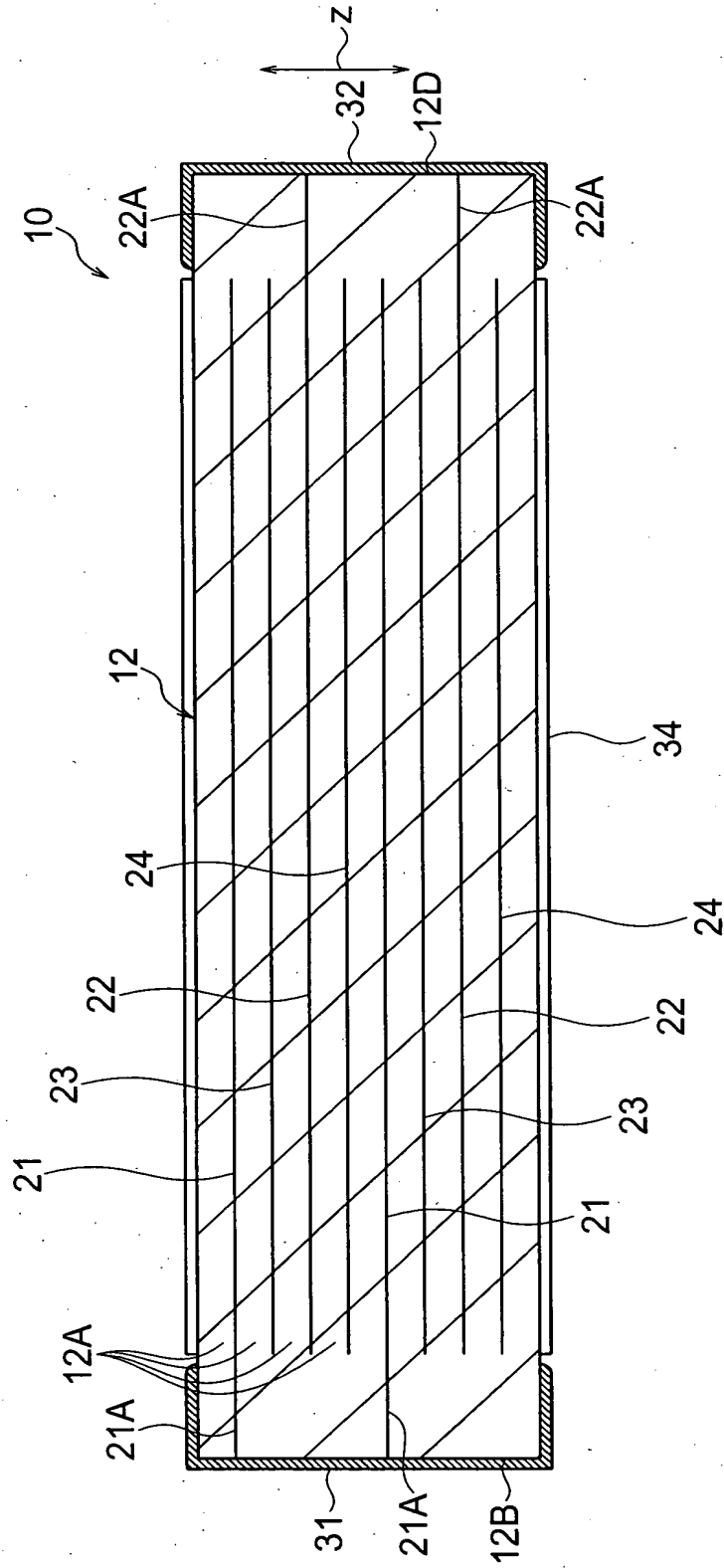


FIG. 4

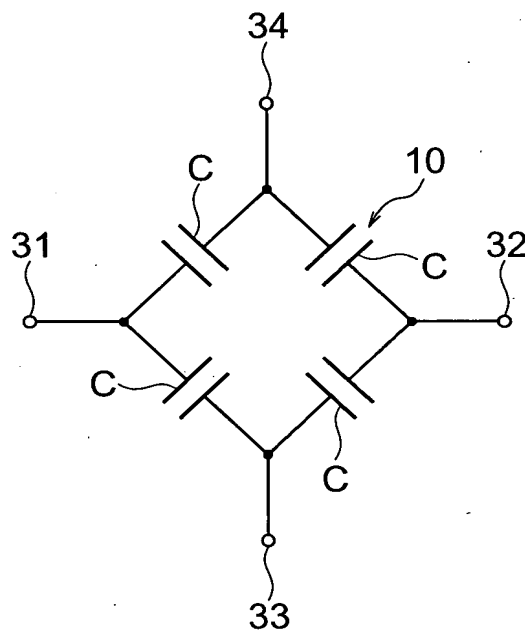


FIG. 6

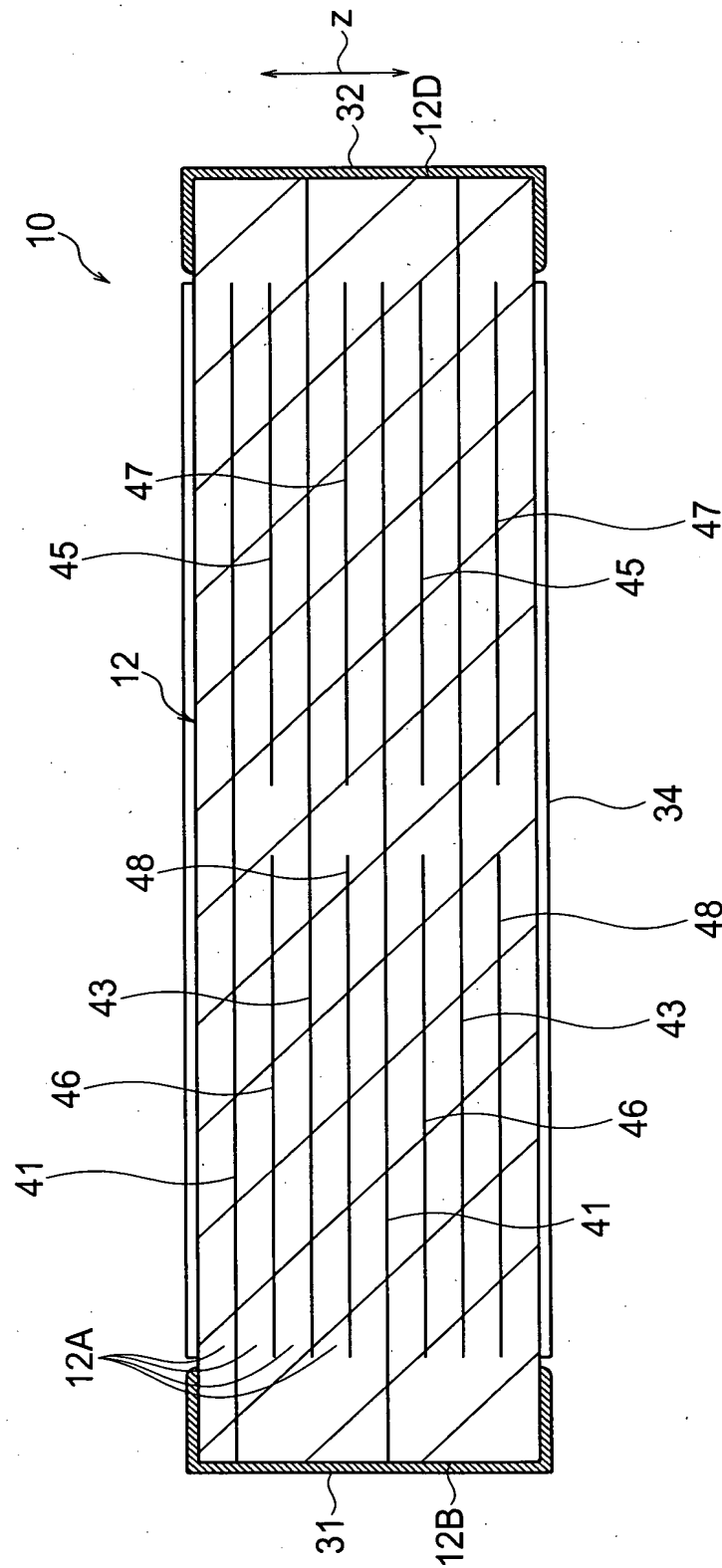


FIG. 7

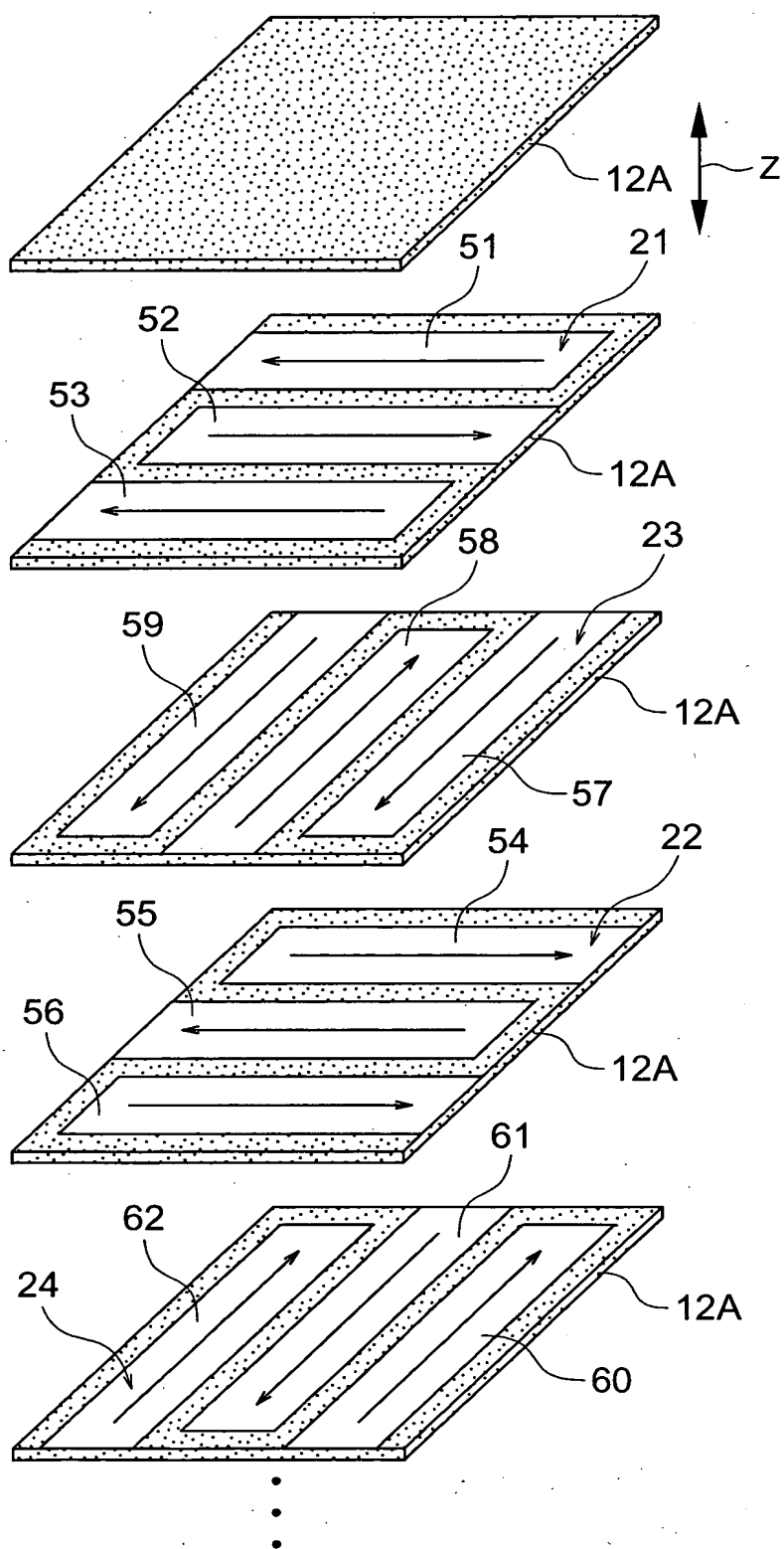


FIG. 8

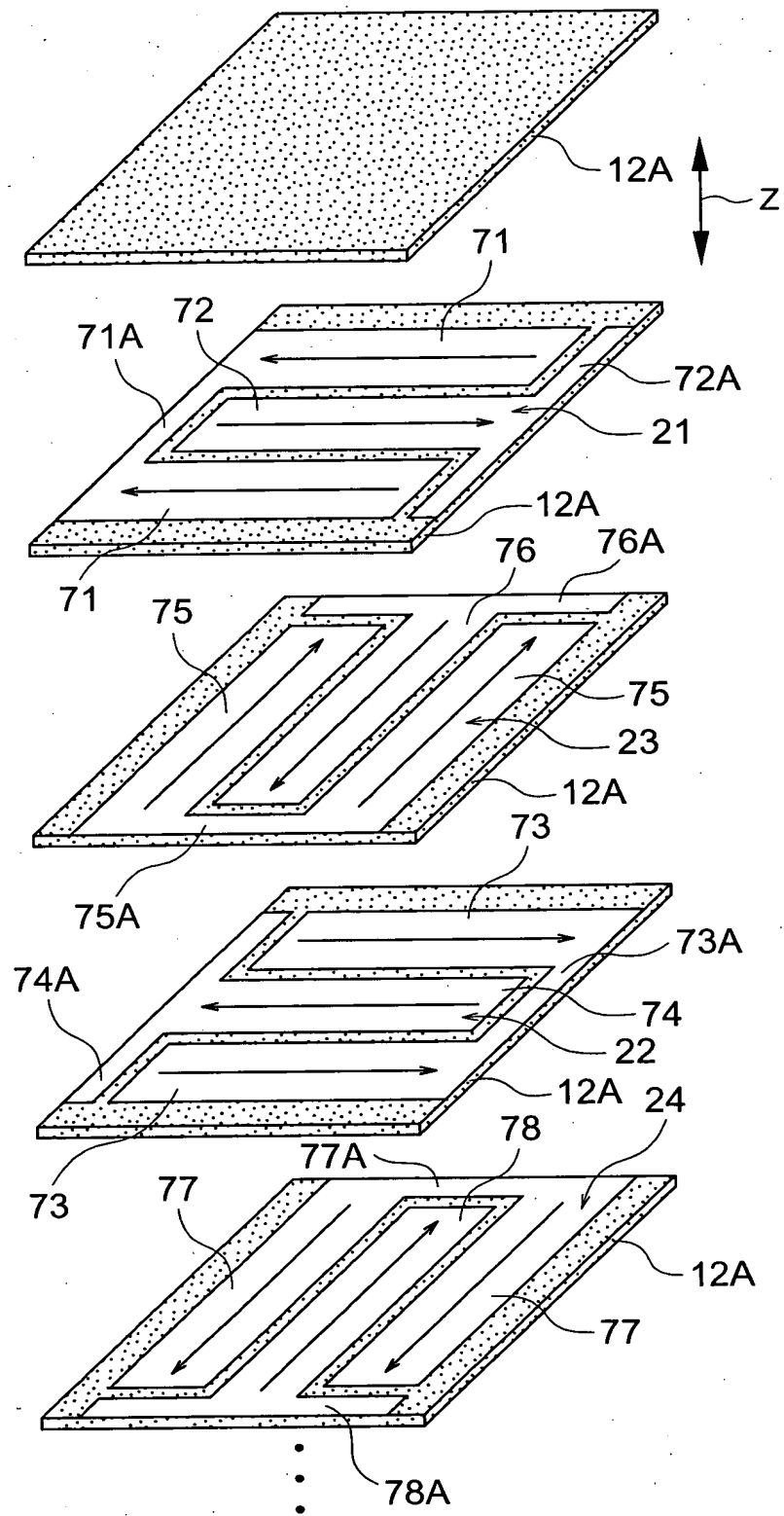


FIG. 9

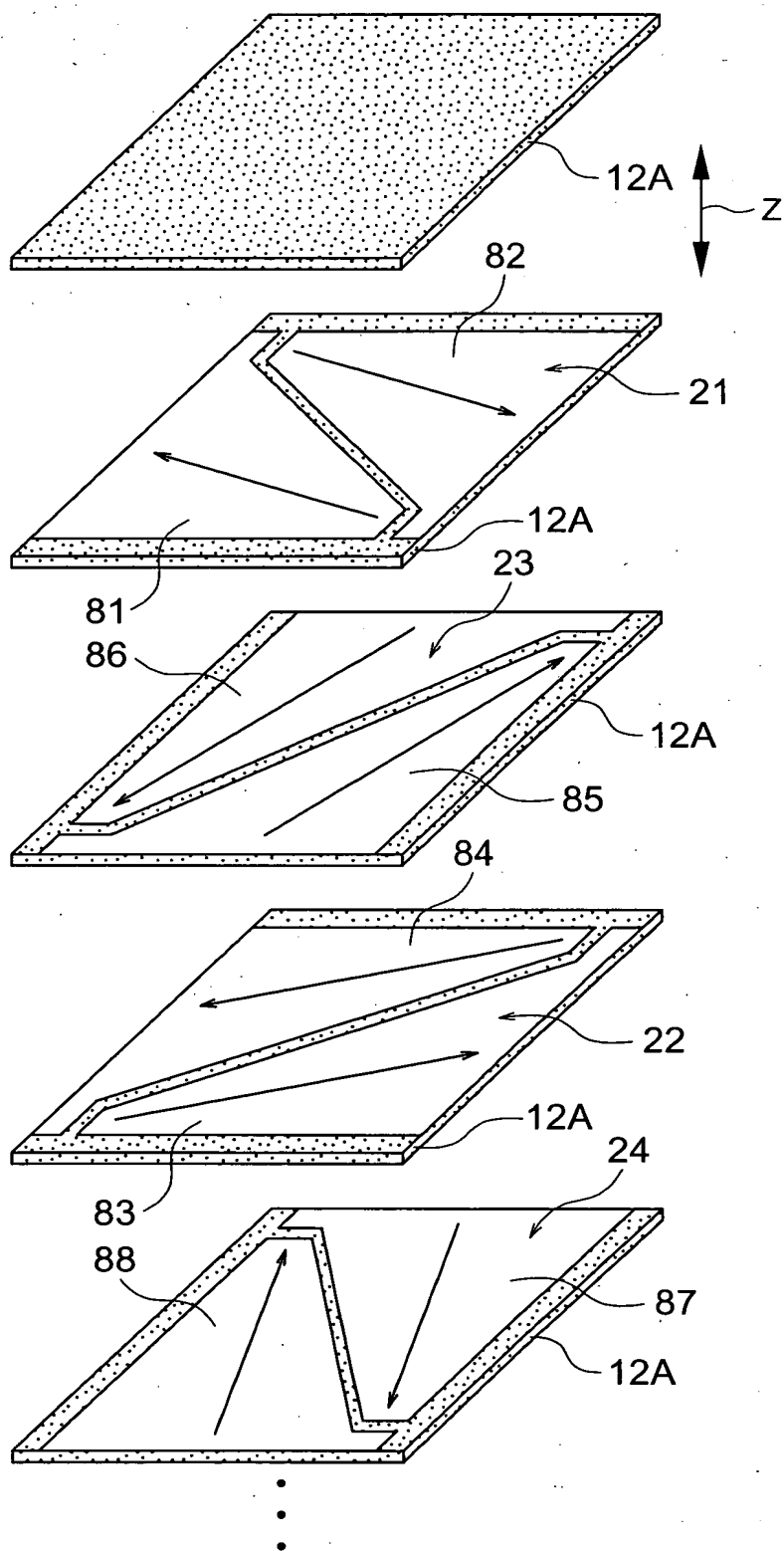


FIG. 10

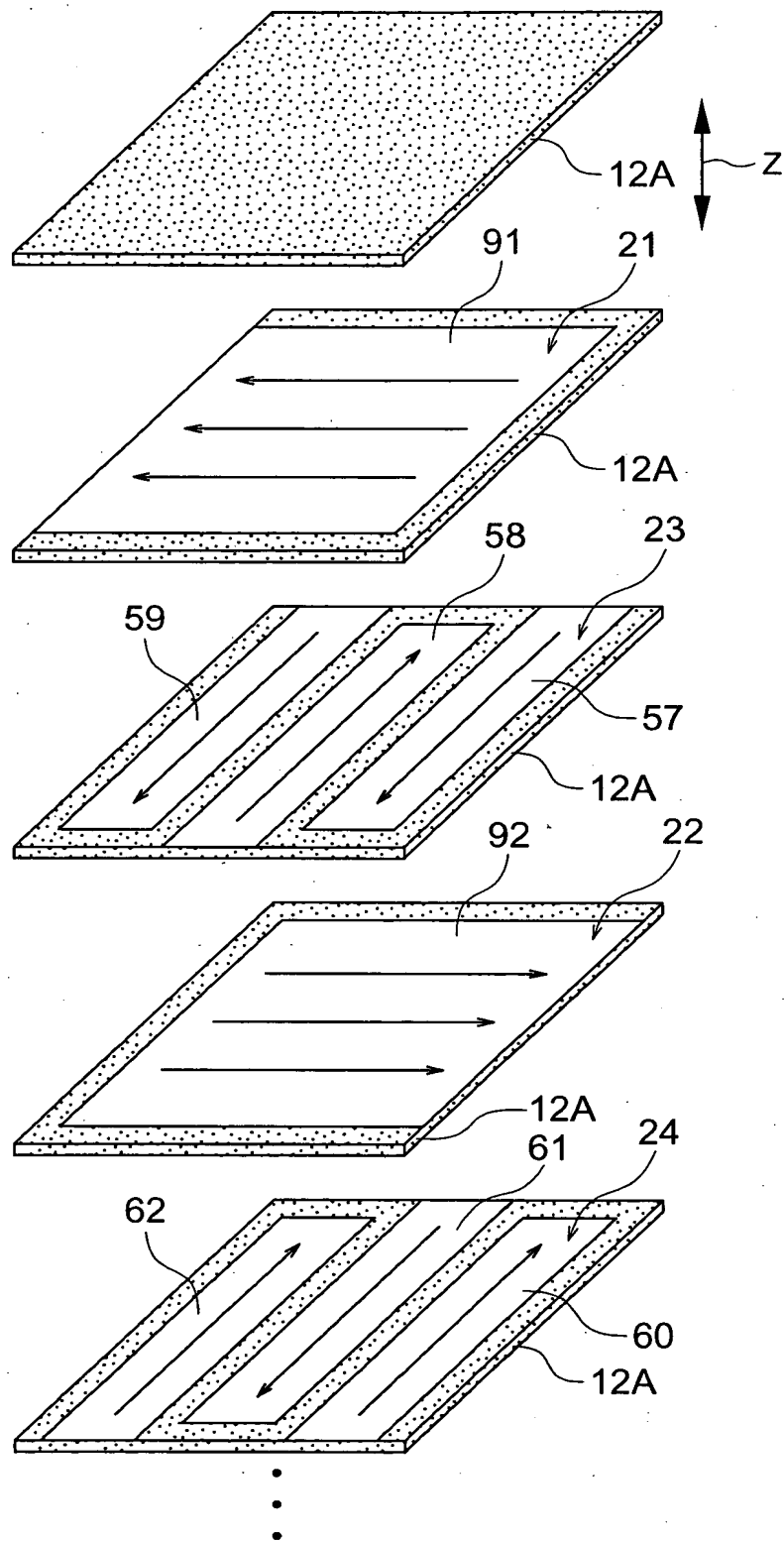


FIG. 11A

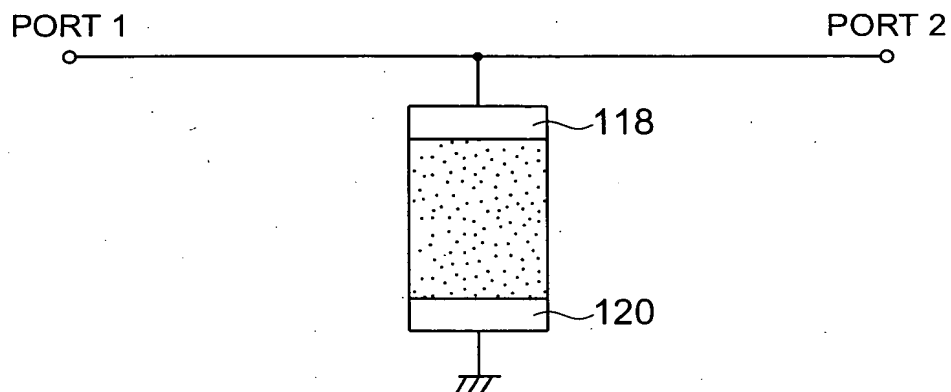


FIG. 11B

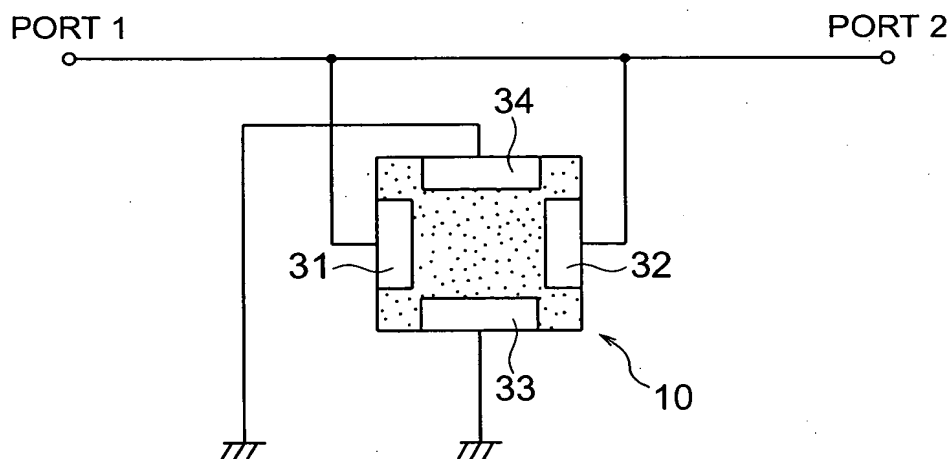


FIG. 12

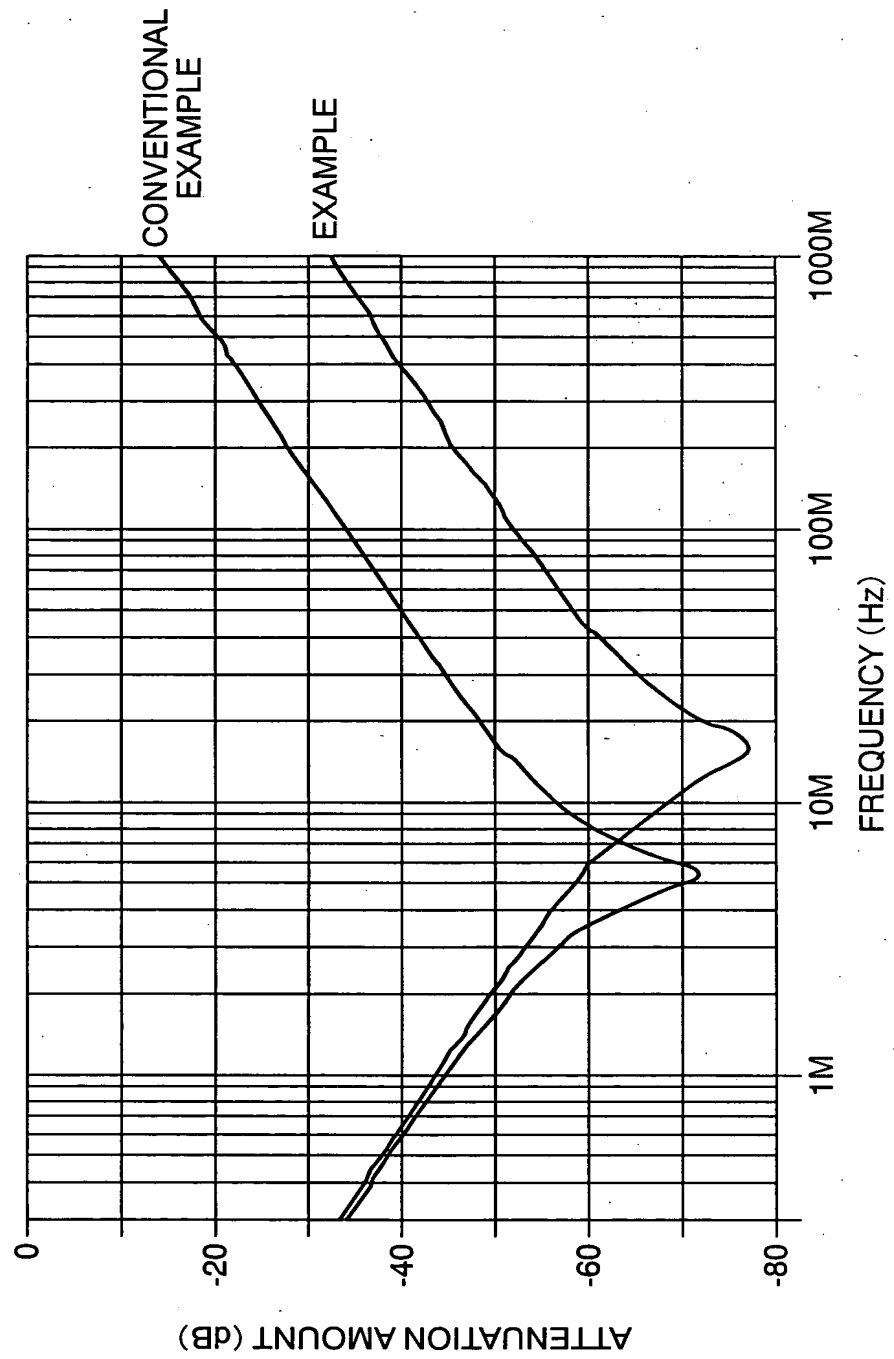


FIG. 13

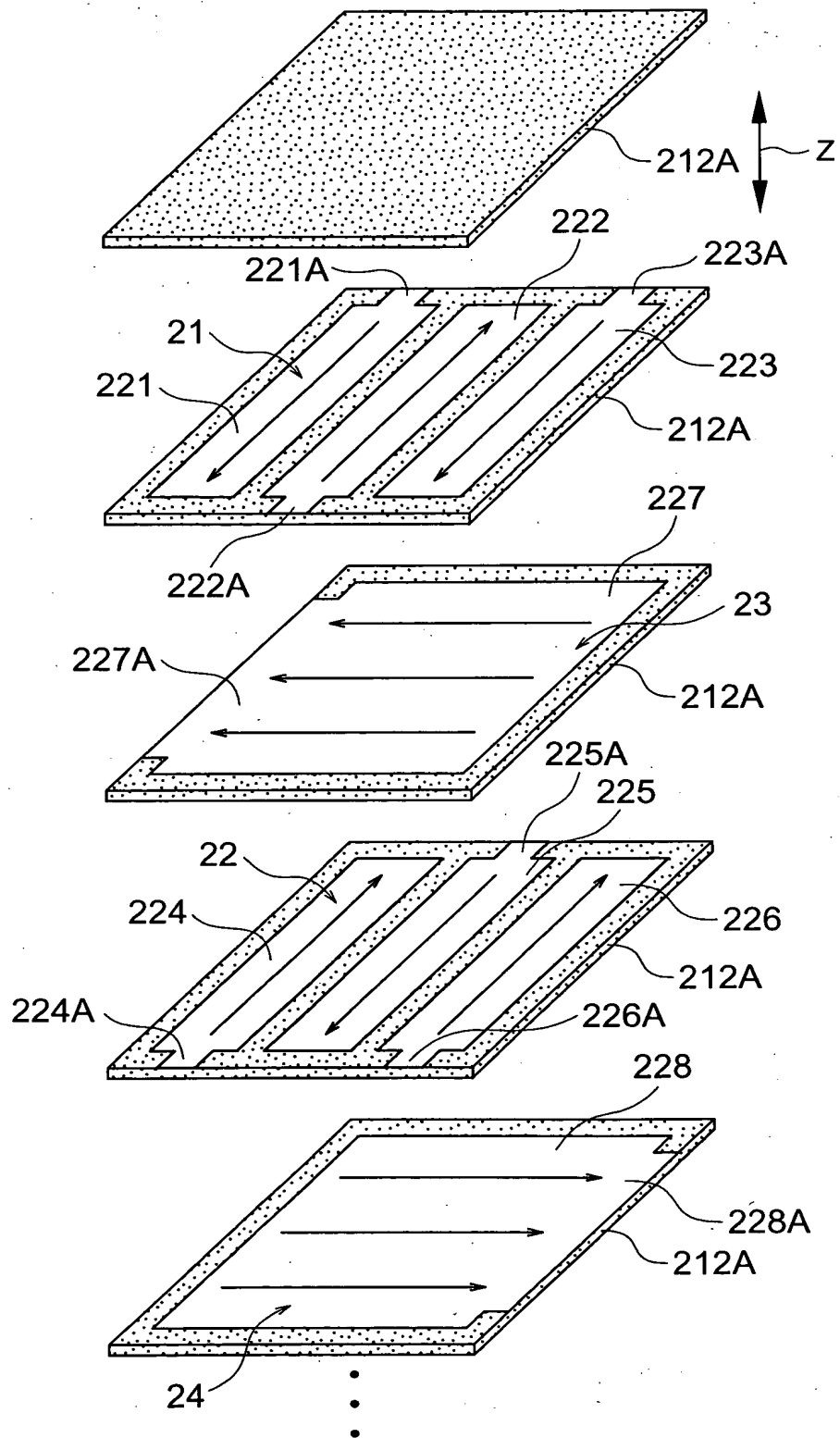


FIG. 15

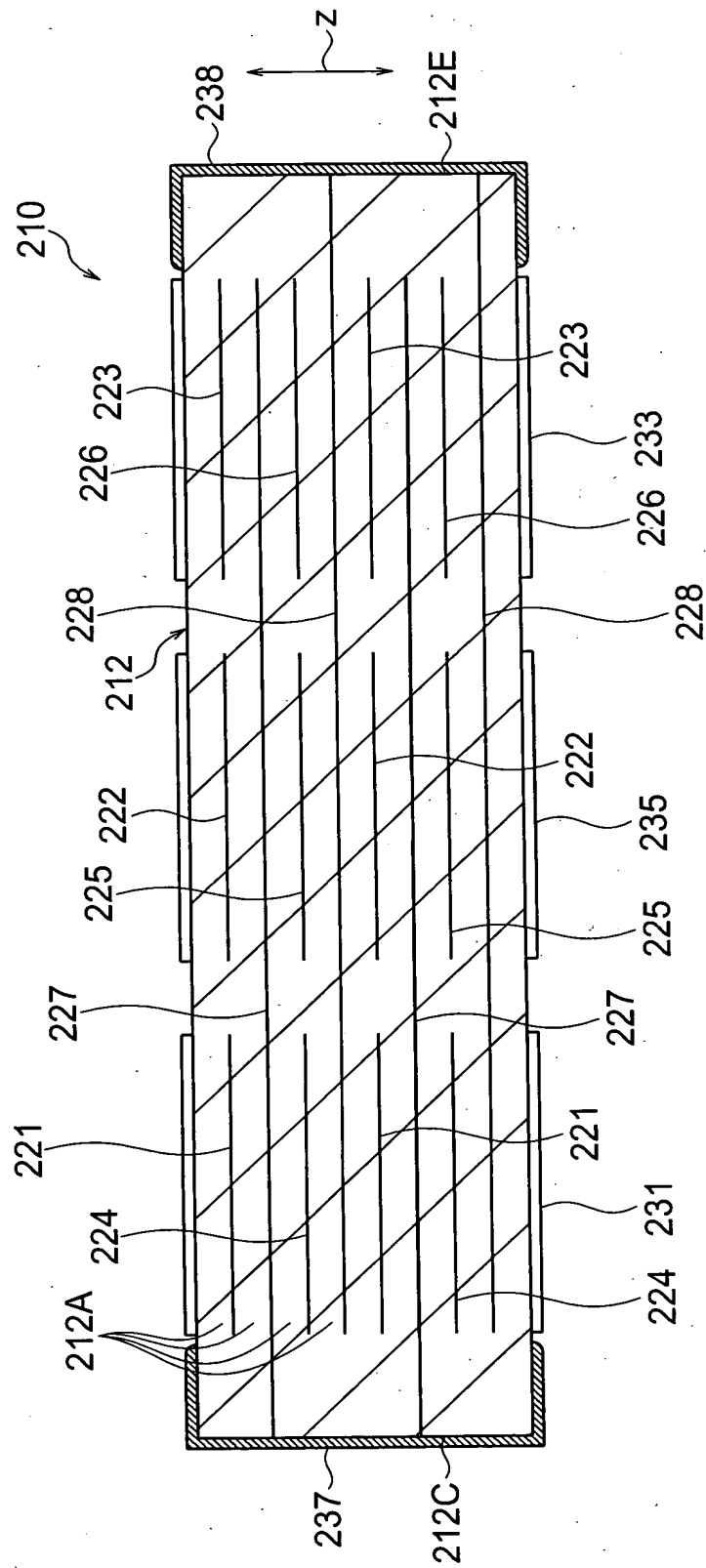


FIG. 16

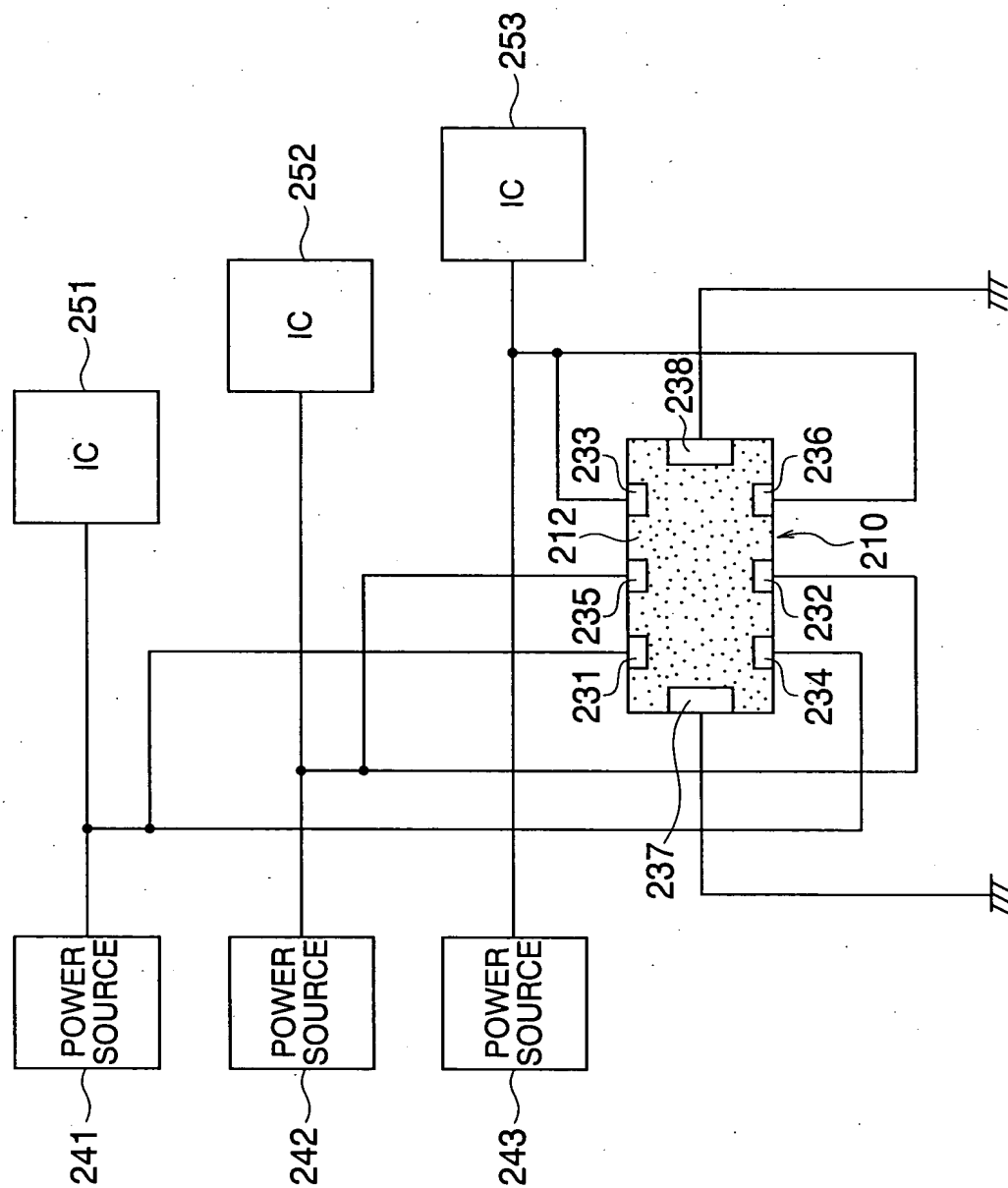


FIG. 17

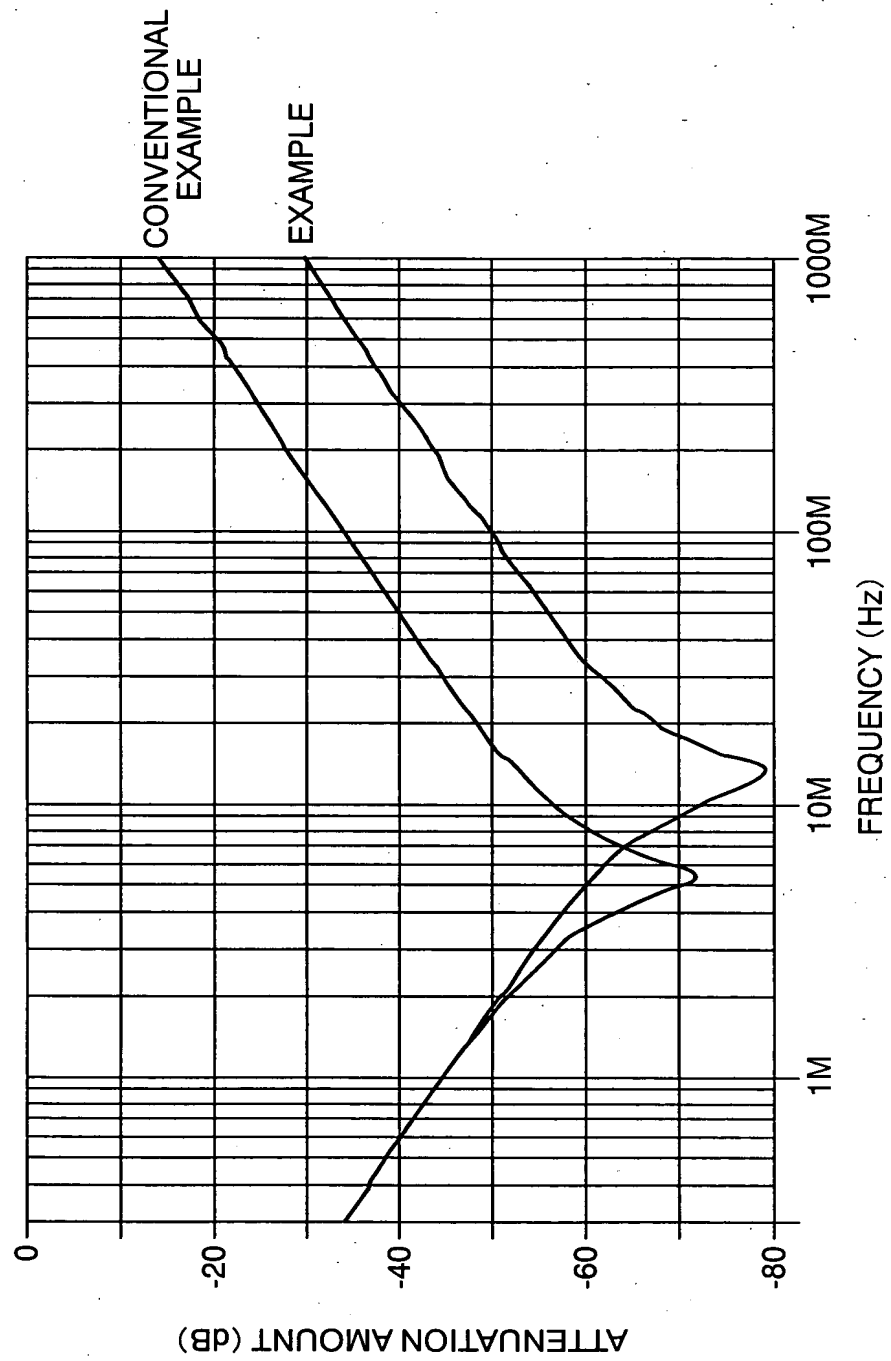
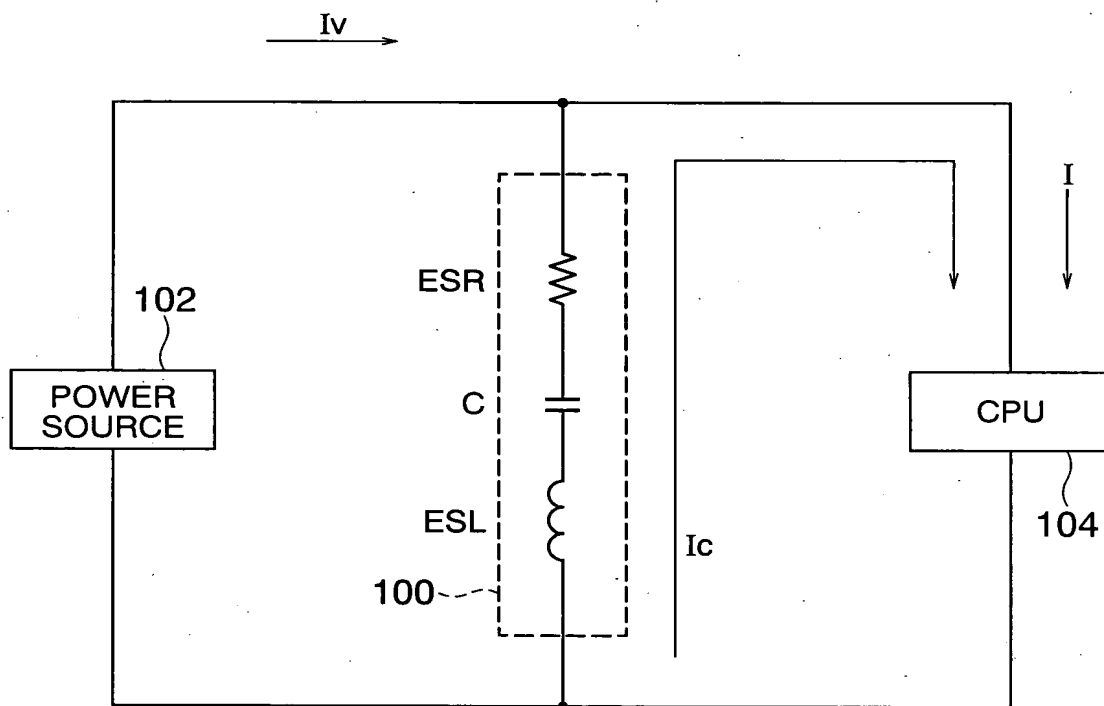


FIG. 18
PRIOR ART



I : DRIVING ELECTRIC CURRENT OF CPU
 I_c : DISCHARGE OF ELECTRIC CURRENT FROM CAPACITOR
 I_v : ELECTRIC CURRENT FROM POWER SOURCE

FIG. 19
PRIOR ART

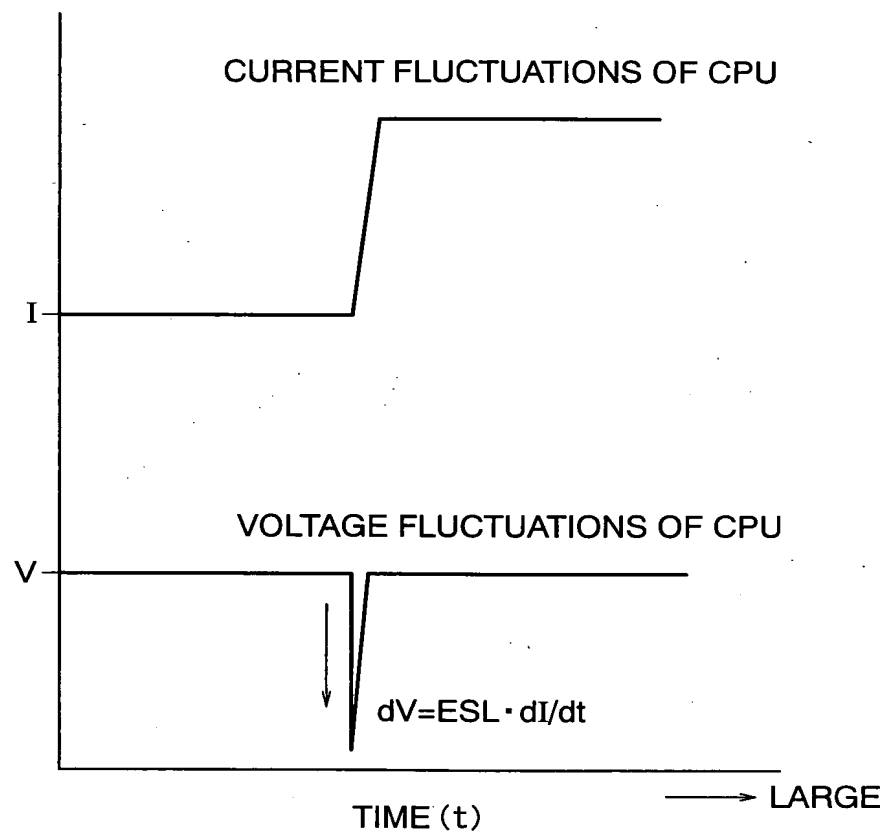
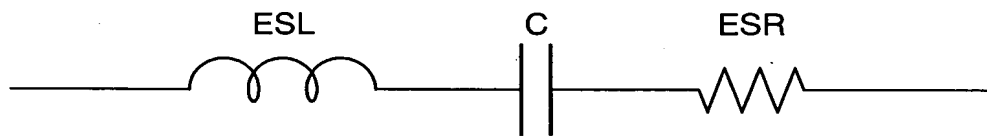


FIG. 20
PRIOR ART



C:CAPACITANCE

ESR:EQUIVALENT SERIAL RESISTANCE

ESL:EQUIVALENT SERIAL INDUCTANCE

FIG. 21
PRIOR ART

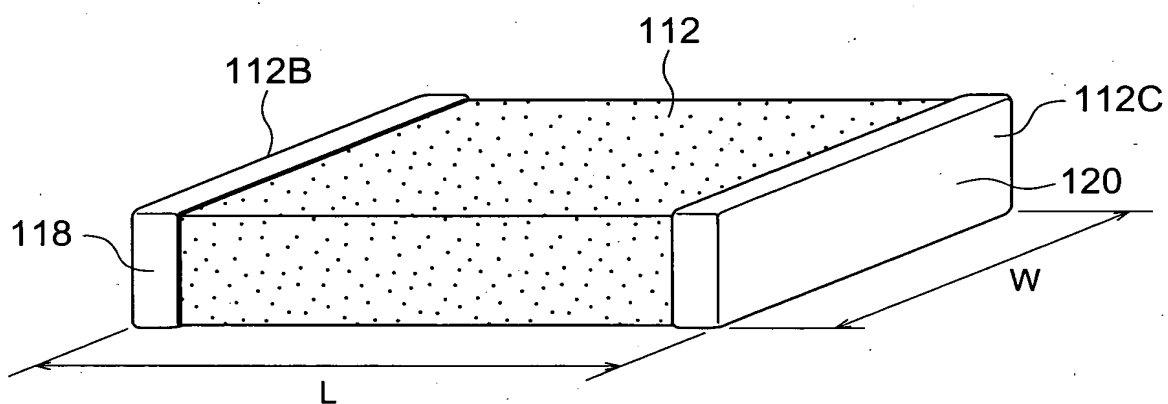


FIG. 22
PRIOR ART

